# Minutes of Meeting of online BOS meeting held on 17.08.2020

As per the 6<sup>th</sup> academic council of HBTU Kanpur the course structure of the B.Tech and M.Tech programs have been revised and according to the approved course structure, we have submitted (through email dated Aug.13, 2020 at 6.49pm) the Proposals of Syllabus for the programs: Bachelor of Technology in Electronics Engineering and Master of Technology in Electronics and Communication Engineering (As per the Ordinances for Bachelor of Technology & Ordinances for Master of Technology as per 6<sup>th</sup> Academic Council) attached as: Final agenda for BOS along with the report of Academic Audits and NBA for kind information please.

For the said purposes the online	<b>BOS</b> meeting	was held on	17.08.2020	from 2	11.00 am.
1. Followings were present in	the meeting				

S.no.	Name	Designation
1.	Dr. Krishna Raj	Professor & Head, ETD
2.	Prof. Kumar Vaibhav Srivastava, Dept of Electrical Engineering IIT Kanpur	BOS Member
3.	Prof. Y. N. Singh, Dept of Electrical Engineering IIT Kanpur	BOS Member
4.	Sri Alok Dixit, Scientist F, SMG, DMSRDE Kanpur	BOS Member
5.	Sri Manoj Kumar Tiwari, Staff Engineer, ST Microelectronics Pvt. Ltd. Plot no. 01, Knowledge Park 3, Greater Noida , UP-201308	BOS Member
6.	Sri Himanshu Baskey, DMSRDE Kanpur	Special Invitee
7.	Prof. Rachna Asthana, Director AITH Kanpur	Special Invitee
8.	Dr. Manoj Kumar Shukla	Professor
9.	Mrs. Rajani Bisht	Associate Professor
10.	Dr. A K Shankhwar	Associate Professor
11.	Dr. Ashutosh Singh	Associate Professor
12.	Dr. Manish Kumar Singh	Assistant Professor
13.	Dr. Suman Kumar Mitra	Assistant Professor
14.	Mr. Partha Saha	Assistant Professor

1:	5. Mr. Dharmendra Kumar Singh	Assistant Professor
10	5. Ms. Nayanica Srivastava	Assistant Professor
1'	7. Dr. Preeti Agarwal Mittal	Guest Faculty
1	3. Dr. Kumar Gaurav	Guest Faculty
1	9. Mr. Deo Chand Jaiswal	Guest Faculty

### 2. Course structure of B. Tech. ET program according to 6<sup>th</sup> Academic Council:

#### Structure of the Curriculum Semester Wise Course Structure & Evaluation Scheme For B. Tech. in Electronics Engineering (Effective from Session 2020-21 for New Entrants: As per the 6<sup>th</sup> Academic Council) <u>I Semester</u>

<b>BSC:</b> Basic Science Course	<b>PEC:</b> Program Elective Course
MC: Mandatory Courses	<b>OEC:</b> Open Elective Course
PCC: Program Core Course	<b>ESC:</b> Engineering Science Course
HSMC: Hum. Social Sc. and Management Courses	

Credits **Sessional Marks** SI. Course Subject Total **Course Title** ESE Code TA Marks No. Type (LTP) MSE Lab Total BSC Physics 20 50 100 1. 4(3-0-2)15 15 50 Mathematics-I 2. BSC 4(3-1-0) 20 50 50 100 30 -3. ESC Electrical 4(3-0-2) 15 20 15 50 50 100 Engineering 4. ESC Engineering 3(3-0-0)30 20 50 50 100 -Mechanics 5. HSMC Professional 3(2-0-2)15 20 15 50 50 100 Communication 6. **HSMC** English Language 2(2-0-0)20 50 50 100 30 and Composition **Total Credits** 20

#### **II Semester**

SI.	Course Subject Course Title		Credits	Se	ession	al Mar	ks	ESE	Total	
No.	Туре	Code	Course The	(LTP)	MSE	ТА	Lab	Total	LSL	Marks
1.	BSC		Engineering	4(3-0-	15	20	15	50	50	100
			Chemistry	2)						
2.	BSC		Mathematics -II	4(3-1-	30	20	-	50	50	100
				0)						
3.	ESC		Electronics &	3(3-0-	30	20	-	50	50	100

		Instrumentation Engineering	0)						
4.	ESC	Engineering Graphics	3(0-0- 6)	30	20	-	50	50	100
5.	ESC	Computer Concept &C Programming	4(3-0- 2)	15	20	15	50	50	100
6.	ESC	Workshop Practice	2(0-0- 4)	-	20	30	50	50	100
7.	MC Non Credit	Environment and Ecology	2(2-0- 0)	30	20	-	50	50	100
		Total Credits	20						

# **III Semester**

Sr.	Course	Subject	Course title	Credits	Sessio	nal M	arks		ESM	Total
No	Туре	code								Marks
					MSE	TA	Lab	Total		
1	BSC		Mathematics- III	4(3-1-0)	30	20	-	50	50	100
2	ESC		Electrical Circuit Analysis	5(3-1-2)	15	20	15	50	50	100
3	PCC		Digital Electronics	4(3-0-2)	15	20	15	50	50	100
4	PCC		Solid State Devices	4(2-1-2)	15	20	15	50	50	100
5	PCC		Hardware Description Language	2(2-0-0)	30	20	-	50	50	100
6	HSMC		Engineering Economics and Management	3(3-0-0)	30	20	-	50	50	100
7	MC (Non- credit)		Indian Constitution	2(2-0-0)	30	20	-	50	50	100
	Te	otal Credit	S				22			

				IV Seme	<u>ster</u>					
Sr. No	Course Type	Subject code	Course title	Credits	Sessio	nal M	ESM	Total Marks		
					MSE	TA	Lab	Total		
1.	BSC		CONM	4(3-1-0)	30	20	-	50	50	100
2.	ESC		Data Structure	5(3-1-2)	15	20	15	50	50	100
			Using C							
3.	PCC		Signal and	3(2-1-0)	30	20	-	50	50	100
			Systems							
4	PCC		Analog Circuits	4(2-1-2)	15	20	15	50	50	100

5.	PCC		Electromagnetic	3(2-1-0)	30	20	-	50	50	100
			Field Theory							
6.	HSMC		Organizational	3(3-0-0)	30	20	-	50	50	100
			Behaviour							
7.	MC		Cyber Security	2(2-0-0)	30	20	-	50	50	100
	(Non-									
	credit)									
	,	Total Cred	22							

# V Semester

Sr.	Course	Subject	Course title	Credits	Se	ession	al Mar	·ks	FSF	Total
No.	Туре	code	course the	Cituits	MSE	TA	Lab	Total	LOL	Marks
1	PCC		Analog Integrated	4(2-1-	15	20	15	50	50	100
1	ICC		Circuits	2)						
2	DCC		Analog	5(3-1-	15	20	15	50	50	100
2	PCC		Communication	2)						
2	DCC		Antenna and Wave	3(2-1-	30	20	-	50	50	100
5	FCC		Propagation	0)						
4	DCC		Microprocessors &	4(2-1-	15	20	15	50	50	100
4	PCC		Microcontroller	2)						
5	DCC		VI SI Taabaalaay	3(2-1-	30	20	-	50	50	100
5	PCC		vLSI Technology	0)						
6	OEC		Onenstian Descend	3(3-0-	20	20		50	50	100
0	(Maths)		Operation Research	0)	50	20	-	- 50	30	100
	Total Credit						22			

#### **VI Semester**

Sr.	Course	Subject	Comme d'Ale	Cara lite	Se	ession	al Mar	ks	ESE	Total Mark
No.	Туре	code	Course the	Credits	MSE	T A	Lab	MSE	ТА	Mark s
1	PCC		Optical Communication	4(2-1-2)*	15	20	15	50	50	100
2	PCC		VLSI Design	3(2-0-2)	15	20	15	50	50	100
3	PCC		Advanced Instrumentation	3(2-1-0)	30	20	-	50	50	100
4	PCC		Digital Communication	3(2-0-2)#	15	20	15	50	50	100
5	PCC		Control System	3(2-1-0)	30	20	-	50	50	100
6	PCC		Machine Learning	3(3-0-0)	30	20	-	50	50	100
7	OEC (Human ities)		OEC (Humanities)	3(3-0-0)	30	20	-	50	50	100
	Total Credit						22			

\*as per the recommendation of BoS, Lab component is introduced in the syllabus and the credit distribution is changes from C(L-T-P) : 4(3-1-0) to C(L-T-P) : 4(2-1-2)

# as per the recommendation of BoS, Lab component is introduced in the syllabus and the credit distribution is changes from C(L-T-P) : 3(2-1-0) to C(L-T-P) : 3(2-0-2)

SI.N	Course	Subje	<b>Course Title</b>	Credits(LT	Ses	ssion	al Ma	rks	ES	Total
О.	Туре	ct		<b>P</b> )	MS	Т	La	Tot	Ε	Mark
		Code			Ε	Α	b	al		S
1.	PCC		Digital	3(2-0-2)	15	20	15	50	50	100
			Signal							
			Processing							
2.	PCC		VLSI	2(2-0-0)	30	20	-	50	50	100
			Implementati							
			on Of Digital							
			Signal							
			Processing							
			Algorithms							
3.	PEC		PEC-I	3(3-0-0)	30	20	-	50	50	100
4.	PEC		PEC-II	3(3-0-0)	30	20	-	50	50	100
5.	OEC		OEC-I	3(3-0-0)	30	20	-	50	50	100
6.	Industri		Industrial	2(0-0-4)	-	50	-	50	50	100
	al		Training							
	Trainin									
	g									
7.	Seminar		Seminar	2(0-0-4)	-	50	-	50	50	100
8.	Project		Project	4(0-0-8)	-	50	-	50	50	100
				22						

#### **VII Semester**

#### VIII Semester

SI.NO	Cours	Subjec	Cours	Credits(LTP	Se	ssion	al Mar	·ks	ES	Total
•	e Type	t Code	e Title	)	MS	Т	La	Tota	Ε	Mark
					Ε	Α	b	l		S
1.	PEC		PEC-	4(3-1-0)	30	20	-	50	50	100
			III							
2.	PEC		PEC-	4(3-1-0)	30	20	-	50	50	100
			IV							
3.	OEC		OEC-II	4(3-1-0)	30	20	I	50	50	100
4.	Project		Project	10(0-0-20)	_	50	-	50	50	100
				22						

\* **Note:** Internal Evaluation of Project in VII semester will be conducted by the Departmental Committee. Evaluation of project in VIII semester will be conducted by External and Internal Examiners.

# **ELECTIVE-I**

SI.N	Cours	Subje	<b>Course Title</b>	Credits(LT	Se	ssion	al Ma	rks	ES	Total
О.	e	ct		<b>P</b> )	MS	Т	La	Tota	Ε	Mark
	Туре	Code			Ε	Α	b	l		S
1.	PEC		Biomedical	3(3-0-0)	30	20	-	50	50	100
			Signal							
			Processing							
2.	PEC		Satellite	3(3-0-0)	30	20	-	50	50	100
			Communicati							
			on							
3.	PEC		Digital	3(3-0-0)	30	20	-	50	50	100
			System							
			Design using							
			VHDL							
4.	PEC		Data	3(3-0-0)	30	20	-	50	50	100
			Communicati							
			on Networks							

# **ELECTIVE-II**

SI.N	Cours	Subje	<b>Course Title</b>	Credits(LT	Se	ssiona	al Ma	rks	ES	Total
О.	e	ct		<b>P</b> )	MS	Т	La	Tota	Ε	Mark
	Туре	Code			Ε	Α	b	l		S
1.	PEC		Artificial	3(3-0-0)	30	20	-	50	50	100
			Intelligence							
2.	PEC		Wireless	3(3-0-0)	30	20	-	50	50	100
			Communicati							
			on							
3.	PEC		VLSI Device	3(3-0-0)	30	20	-	50	50	100
			Modelling							
4.	PEC		Microwave	3(3-0-0)	30	20	-	50	50	100
			and Radar							
			Engineering							

#### **ELECTIVE-III**

SI.N	Cours	Subje	<b>Course Title</b>	Credits(LT	Se	ssion	al Ma	rks	ES	Total
О.	e	ct		<b>P</b> )	MS	Т	La	Tot	Ε	Mar
	Туре	Code			Ε	Α	b	al		ks
1.	PEC		Architect	4(3-1-0)	30	20	-	50	50	100
			ure And							
			Applicati							
			ons Of							
			Digital Signal							
			Processors							

2.	PEC	Information	4(3-1-0)	30	20	-	50	50	100
		Theory and							
		Coding							
3.	PEC	Advanced	4(3-1-0)	30	20	-	50	50	100
		Semiconductor							
		Devices							
4.	PEC	RF Systems	4(3-1-0)	30	20	-	50	50	100

### **ELECTIVE-IV**

SI.NO	Cours	Subjec	Course	Credits(LTP	Se	ssion	al Ma	rks	ES	Total
•	e Type	t Code	Title	)	MS	Т	La	Tota	Ε	Mark
					Ε	Α	b	l		S
1.	PEC		Image	4(3-1-0)	30	20	-	50	50	100
			Processin							
			g							
2.	PEC		Neural	4(3-1-0)	30	20	-	50	50	100
			Networks							
3.	PEC		Embedde	4(3-1-0)	30	20	-	50	50	100
			d Systems							
4.	PEC		Data	4(3-1-0)	30	20	-	50	50	100
			Analytics							

# **OPEN ELECTIVE-I**

SI.N	Cours	Subje	<b>Course Title</b>	Credits(LT	Se	ssion	al Ma	rks	ES	Total
О.	e	ct		<b>P</b> )	MS	Т	La	Tota	Ε	Mark
	Туре	Code			Ε	Α	b	l		S
1.	OEC		Mobile	3(3-0-0)	30	20	-	50	50	100
			Communicati							
			on							
2.	OEC		Biomedical	3(3-0-0)	30	20	-	50	50	100
			Electronics							

# **OPEN ELECTIVE-II**

SI.NO	Cours	Subjec	Course	Credits(LT	Se	ssion	al Ma	rks	ES	Total
•	e	t Code	Title	<b>P</b> )	MS	Т	La	Tota	E	Mark
	Туре				Ε	Α	b	1		S
1.	OEC		Image	4(3-1-0)	30	20	-	50	50	100
			Processing							
2.	OEC		Fuzzy	4(3-1-0)	30	20	-	50	50	100
			Logic with							
			electronics							
			engineerin							
			g							
			application							
			S							

3. Course structure of M. Tech. ET program according to 6<sup>th</sup> Academic Council:

#### Structure of the Curriculum Semester Wise Course Structure & Evaluation Scheme For B. Tech. in Electronics and Communication Engineering (Effective from Session 2020-21 for New Entrants: As per the 6<sup>th</sup> Academic Council)

Sr. No	Course Type	Subject code	Course title	Credits	Sessio	nal M	arks		ESM	Total Marks
					MSE	TA	Lab	Total		
1.	PCC		Introduction to	5(3-2-0)	30	20	-	50	50	100
			Signal Analysis							
2.	PCC		Estimation and	4(3-1-0)	30	20	-	50	50	100
			Detection							
			Theory							
3.	PCC		Advanced	5(3-2-0)	30	20	-	50	50	100
			Semiconductor							
			Devices							
4.	PCC		Neural Network	4(3-1-0)	30	20	-	50	50	100
		Total Credit	S				18			

#### **I** Semester

### **II Semester**

Sr. No	Course Type	Subject code	Course title	Credits	Sessio	nal M	larks		ESM	Total Marks		
					MSE	TA	Lab	Total				
1.	PCC		Digital	4(3-1-0)	30	20	-	50	50	100		
			Communication									
2.	PCC		Optical	4(3-1-0)	30	20	-	50	50	100		
			Communication									
3.	PEC		PEC-1	4(3-1-0)	30	20	-	50	50	100		
4.	PEC		PEC-2	4(3-1-0)	30	20	-	50	50	100		
	,	Total Credit	S		16							

#### **III Semester**

Sr. No	Course Type	Subject code	Course title	Credits	Sessio	nal M	larks		ESM	Total Marks
					MSE	TA	Lab	Total		
1.	PCC		Advanced	4(3-1-0)	30	20	-	50	50	100
			Digital Signal							
			Processing							
2.	PEC		PEC-3	4(3-1-0)	30	20	-	50	50	100
3.	Seminar		-	2(0-0-4)	-	50	-	50	50	100
4.	Dissertation		-	4(0-0-8)	-	50	-	50	50	100
	Tot	tal Credits					14			

#### **IV Semester**

Sr. No	Course Type	Subject code	Course title	Credits	Sessio	nal M	arks		ESM	Total Marks
					MSE	TA	Lab	Total		
1.	Dissertation		-	12(0-0-	-	50	-	50	50	100
				24)						
	Tot	tal Credits								

#### In the above series, the following responses were received from respected members:

# Following points were suggested (for syllabus of EET-101/102) by respected BOS members in the online meeting:

1. Weightage of MOSFET must be embedded in Unit-II for its industrial prospects.

2. Topic 'modern display technique- OLED, AMOLED, PDP, QLED displays' must be incorporated in Unit –V.

3. Behzad Razavi/ "Fundamentals of Microelectronics"/ Wiley may be added as reference book.

Above points have been incorporated in the syllabus of B.Tech. First year as approved by BoS members present in the meeting.

# Following points were suggested (for syllabus of II B. Tech. ET subjects) by respected BOS members in the online meeting:

# Minuets of the BoS meeting 2<sup>nd</sup> year B. Tech.

In the BoS meeting for Department of Electronics Engineering held on 17<sup>th</sup> August 2020 at 11.00 AM in an online mode, the following suggestion has been provided by the BoS Committee members:

- 1. In the EMFT subject, the reference book of David K. Cheng should be in the text book section.
- 2. The subject Analog Electronics or Analog Circuits can be taught in the 5<sup>th</sup> Semester
- 3. The subject Control System may be included in the 4<sup>th</sup> semester.
- 4. Suggested to add FDSOI and PDSOI in the syllabus of Solid state devices.
- 5. The effect of poles and zeros in negative feedback can be added in the 3<sup>rd</sup> unit of subject Analog Circuits.
- 6. Recommended to add the books by Allen and Holberg, Razavi and the lecture series by Razavi for the subject Analog Circuits.

- 7. Addition of small signal analysis and large signal analysis ( $\pi$  model) etc. in the subject Analog Circuits is recommended.
- The topic of Impedance and gain analysis of amplifier may be added in the subject of Analog Circuits.
- 9. As per the member of BoS, the changes made in the subject Signals and system syllabus is not needed, since, first part is necessary to students that will provide the base of subject. Only Laplace transform can be removed as students already studying in Mathematics II.
- 10. As suggested by a BoS member the subject Mathematics III may be removed and signals and systems subject can be introduced in III Semester in place of Mathematics III, which will provide good base to students and good learning to other subjects in III<sup>rd</sup> and subsequent semesters.
- 11. As per the suggestion by a BoS member, Control Systems which is currently in 6<sup>th</sup> semester should be taught in 4<sup>th</sup> semester that brings concepts of stability in amplifiers and other electronic devices. So, by removing Mathematics III, signals and systems can be taken into III<sup>rd</sup> Semester and in place of signals and system, control system can be taught in the 4<sup>th</sup> semester.

# Following points were suggested (for syllabus of III B.Tech. ET subjects) by respected BOS members in the online meeting:

- 12. ANALOG COMMUNICATION syllabus should include the Analog communication Circuits (Transmitter and receiver circuit), Intermediate Frequency, Double Heterodyne Receiver and Analog receiver.
- 13. ANALOG COMMUNICATION Experiment list should include the transmitter and receiver experiments (no Kit base experiment, students need to prepare their transmitting and receiver circuit by themselves in Lab)
- 14. In the ANTENNAS AND WAVE PROPOGATION syllabus 'Antenna Principles' sections should discuss Parabolic and loop antenna along with Horn antenna, Patch antenna.
- 15. In the ANTENNAS AND WAVE PROPOGATION syllabus should contain Retarded potential, linear and binomial array, Return Loss of antenna
- 16. "Antenna Theory Analysis and Design, by C. A. Balanis" should be included in the TEXT book list of ANTENNAS AND WAVE PROPOGATION

- 17. MICROPROCESSOR subject to be renamed as MICROPROCESSOR & MICROCONTROLLER.
- 18. MICROPROCESSOR syllabus should contain AMR process (30% 50% of the course), small portion of the 8051 microcontroller can be the there in the ARM section.
- 19. MICROPROCESSOR syllabus should contain cross compiler, RISC processor
- 20. MICROPROCESSOR experiments list should include sufficient amount of ARM based experiments
- 21. OPTOELECTRONICS can be replaced by the OPTICAL COMMUNICATION & SWITCHING NETWORK Subject with credit distribution of C(L-T-P) : 4(2-1-2)
- 22. DIGITAL COMMUNICATION can be teach with credit distribution as C(L-T-P) : 3(2-0-2).
- 23. In CONTROL SYSTEM Syllabus should include in "State variable Analysis" section, the Kalman Base Control.
- 24. BOS recommended to teach the CONTROL SYSTEM and SIGNAL SYSTEM Subject before teaching the ANALOG ELECTRONICS that brings concepts of stability in amplifiers and other electronic devices

## Minutes of the BoS meeting 4<sup>th</sup> year B. Tech.

- Recommendation of changing name from 'Radar and Microwave Engg.' to 'Microwave and Radar Engg.' was suggested for better clarity. In the MICROWAVE AND RADAR ENGINEERING syllabus following topics need to be added and to upgrade the syllabus also.
  - a) S-parameters
  - b) Microstrip Line
  - c) Cyclotrons, directional coupler
  - d) T-junctions
  - e) Radar imaging
  - f) High Power Devices
  - g) Microwave filters
- 2. In PEC-II (Elective –II) the subject of 'COMPUTER NETWORKS' is suggested to be replaced by subject "DATA COMMUNICATION NETWORKS"

- 3. BoS recommended to include the topic of "Kalman Filtering" concept in ARTIFICIAL INTELLIGENCE Syllabus.
- BOS recommended to verify the syllabus content of subjects 'MACHINE LEARNING' of B.Tech (2<sup>nd</sup> year) and 'ARTIFICIAL INTELLIGENCE' of B.Tech Final year (Elective) to prevent overlapping of topics. It was however checked and verified.
- BoS suggested to include a subject 'OPTICAL COMMUNICATION AND SWITCHING' as an elective subject (PEC). But Finally it got included in 6<sup>th</sup> Semester.
- 6. BoS strongly recommended to modify/upgrade syllabus of subjects with new advance topics inclusions and suggested removal of obsolete topics from syllabus of subjects in all.

### Minutes of the BoS meeting M. Tech (1<sup>st</sup> and 2<sup>nd</sup> year) M. Tech.

1. Following topics can be included in the Introduction to Signal Analysis (EET-501):

Image as a Signal, Sampling of Image, Signal Analysing using Wavelet Transform

- 2. For Mobile Communication (EET-663) syllabus is also need to include recent technologies: GPRS, EDGE, UMTS, LTE and LTE-Advanced.
- 3. For the following subject's syllabus is very limited, need to revised thoroughly.
  - (i) Advanced Semiconductor Devices (EET-503)
  - (ii) Digital Communication (EET-502)
  - (iii) Advanced Digital Signal Processing (EET-601)
  - (iv) Optical Communication (EET-504)
  - (v) Analog VLSI Circuits (PCC)
  - (vi) Space Communication (EET-551)
  - (vii) Antennas Analysis & Synthesis (EET-552)
  - (viii) Image Processing (EET-653)
  - (ix) Data Communication Networks (EET-655)
  - (x) Wireless Communication (EET-556)
  - (xi) VLSI System Design (EET-554)
  - (xii) Communication Theory (EET-563)
- **1.** During online BOS meeting with all members following suggestions are given:
  - a. In the subject Introduction to Signal Analysis (ISA) syllabus include spatio temporal signal suggested by Dr. Y. N. Singh.
  - b. In the Digital vlsi circuits include Design of operational Amplifier suggested by Dr. Y. N. Singh and include chapters of Jan M Rabaey, Anantha Chandrakasan,

Borivoje Nikolic, "Digital Integrated Circuits: A Design Perspective" Pearson Education as part of syllabus suggested by Mrs. Rajani Bisht.

- c. Estimation and Detection Theory subject will be run as PCC 2 in M.Tech 1<sup>st</sup> Semester in 4 credits in 4(3-1-0) manner suggested by Dr. Y.N Singh, Dr. Rachna Asthana, Mrs. Rajani Bisht and other BOS members.
- d. Subject Analog VLSI Circuits will be include as PEC-1 subject in 4 credits (3-1-0) Suggested by BOS members.
- e. In Optical communication include optical amplifier, wavelength converter, Dispersion analysis, Link budget and Modulators suggested by Dr. Y.N Singh.
- f. Course outcomes should be before syllabus suggested by Dr. Y.N. Singh.
- g. In RF System include book of RF Microelectronics by Behzad Razavi suggested by Mrs. Rajani Bisht.
- h. A new subject Introduced titled "Neural Network" in the M. Tech first Year (1<sup>st</sup> Semester) in PCC 4 with 4(3-1-0) credit and run in M. Tech 1<sup>st</sup> semester.