M. TECH

ELECTRONICS & COMMUNICATION ENGINEERING

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **S No** | **Course Code** | **Subject** | **Periods** | **Evaluation Scheme** | **Subject Total** |
| **Sessional** | **Examination**  |
|  |  | **Theory** | **L** | **Tut. / Prac.** | **CT**  | **Attendance** | **TA** | **Total** | **ESE** |  |
| 1. |  | Introduction to Signal Analysis | 3 | 1 | 30 | 10 | 10 | 50 | 100 | 150 |
| 2. |  | Advanced Semiconductor Devices | 3 | 1 | 30 | 10 | 10 | 50 | 100 | 150 |
| 3. |  | Probability Statistics and Queuing Model | 3 | 1 | 30\* | 10 | 10 | 50 | 100 | 150 |
| 4. |  | Elective I | 3 | 1/2 | 30\* | 10 | 10 | 50 | 100 | 150 |
|  |  | Total= | 12 | 6 |  |  |  | 200 | 400 | 600 |

\* 15 marks are for class test and 15 marks are for lab if any, otherwise 30 marks are for class test

Year:1

SEMESTER II

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **S No** | **Course Code** | **Subject** | **Periods** | **Evaluation Scheme** | **Subject Total** |
| **Sessional** | **Examination**  |
|  |  | **Theory** | **L** | **Tut. / Prac.** | **CT**  | **Attendance** | **TA** | **Total** | **ESE** |  |
| 1. |  | Digital Communication | 3 | 2 | 30 | 10 | 10 | 50 | 100 | 150 |
| 2. |  | Optical Communication | 3 | 1 | 30 | 10 | 10 | 50 | 100 | 150 |
| 3. |  | Discrete Mathematics  | 3 | 1/2 | 30\* | 10 | 10 | 50 | 100 | 150 |
| 4. |  | Elective II | 3 | 1/2 | 30\* | 10 | 10 | 50 | 100 | 150 |
|  |  | Total= | 12 | 6 |  |  |  | 200 | 400 | 600 |

Year: II

SEMESTER III

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **S No** | **Course Code** | **Subject** | **Periods** | **Evaluation Scheme** | **Subject Total** |
| **Sessional** | **Examination**  |
|  |  | **Theory** | **L** | **Tut. / Prac.** | **CT**  | **Attendance** | **TA** | **Total** | **ESE** |  |
| 1. |  | Advanced Digital Signal Processing | 3 | 1 | 30 | 10 | 10 | 50 | 100 | 150 |
| 2. |  | Open Elective | 3 | 1 | 30 | 10 | 10 | 50 | 100 | 150 |
| 3. |  | Seminar/Minor Project | - | 2 | - | - | - | 100 | - | 100 |
| 4. |  | Dissertation\* | - | 8 | - | - | - | 50 | - | 50 |
|  |  | Total= | 6 | 12 |  |  |  | 200 | 200 | 400 |

Year: II

SEMESTER IV

|  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- |
| **S No** | **Course Code** | **Subject** | **Periods** | **Evaluation Scheme** | **Subject Total** |
| **Sessional** | **Examination**  |
|  |  | **Theory** | **L** | **Tut. / Prac.** | **CT**  | **Attendance** | **TA** | **Total** | **ESE** |  |
| 1. |  | Dissertation | - | 18 | - | - | - | 150 | 200 | 350 |
|  |  | Total= |  | 18 |  |  |  | 150 | 200 | 350 |

# List of Electives for M.Tech Electronics & Communication

## Elective-I

|  |  |  |
| --- | --- | --- |
| **Sl. No.** | **Course Code** | **Name of the course** |
|  |  | Satellite Communication |
|  |  | Organic Electronics |
|  |  | Microwave Engineering |
|  |  | VHDL  |
|  |  | Advanced Microprocessor |
|  |  | Communication Theory |

**Elective-II**

|  |  |  |
| --- | --- | --- |
| **Sl. No.** | **Course Code** | **Name of the course** |
|  |  | Antenna Analysis & Synthesis |
|  |  | VLSI System Design |
|  |  | Data Communication Networks |
|  |  | Information Theory & Coding |
|  |  | Architecture & Applications of Digital Signal Processors |
|  |  | Wireless Communication |
|  |  | Radar Engineering |
|  |  | Embedded Systems |
|  |  | VLSI Implementation of Digital Processors |

### Open Elective

|  |  |  |
| --- | --- | --- |
| **Sl. No.** | **Course Code** | **Name of the course** |
|  |  | Telecommunication System Engineering |
|  |  | Image Processing |
|  |  | Mobile Communication |
|  |  | Embedded Systems |
|  |  | Fuzzy Electronics  |
|  |  | Professional Communication |
|  |  | Biomedical Signal Processing |