

Publications of Ms. Rajani Bisht

Research Papers Published/ Presented in International Conferences

| Sr. No. | Authors | Title of Paper | Name of Organizer (Institution) | Name of University | Conference Date and Year |
|---------|--------------------------|--|---|--------------------|---|
| . | Rajani Bisht, B. Mazhari | “Impact of Kink Effect on Performance of Poly-Silicon based TFT Differential Amplifiers” | Asian Symposium on Information Display (ASID) | IIT Kanpur | 06 in New Delhi during Oct 8 to Oct 12, 06. |

Research Papers Published/ Presented in National Conferences

| Sr. No. | Authors | Title of Paper | Name of Organizer (Institution) | Year of Publication |
|---------|-------------------------------------|---|---------------------------------|---|
| | Rajani Bisht, S Sarkar, R P Agarwal | “VLSI interconnect delay minimization using CMOS inverters” | IIT, Roorkee | Proceedings of All India Seminar on Recent Trends in VLSI, (held on 29 th -30 th September 2001 |