

DR. AKSHIT SAMADHIYA

Ph.D. (Electrical Engineering), NIT Jamshedpur | M.Tech (PED) | B.Tech (EEE)

Assistant Professor, Electrical Engineering

IEEE Member | IAENG Member

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PROFILE SUMMARY

Highly analytical and outcome-oriented professional with over 4 years of experience in multidisciplinary engineering domain (Electrical, Electronics, AI&ML), Outcome-Based Education (OBE) frameworks, NAAC/NBA accreditation management, Research and development. Experienced in managing NAAC/NBA accreditation activities, including SSR/SAR preparation, CO-PO-PSO mapping and attainment, compliance monitoring, and continuous quality improvement initiatives. Demonstrated ability to design and assess outcome-based curricula, evaluate learning outcomes, and support institutional quality enhancement through effective documentation and compliance management. Possesses strong research expertise in power systems planning, renewable energy integration, optimization techniques, and intelligent engineering applications, supported by excellent technical writing, analytical, and organizational capabilities.

PROFESSIONAL EXPERIENCE

Assistant Professor, Electrical Engineering

(August 2024 – July 2026)

School of Engineering and Technology, Sandip University

Responsibilities:

- Delivering theory, laboratory, tutorial, and project-based courses for B.Tech/M.Tech/Ph.D. students.
- Planning and organizing seminars, workshops, industrial visits, expert/guest lectures, and other academic enrichment activities.
- Guiding and mentoring B.Tech. and M.Tech. students in mini-projects, major projects, internships, and placement-related activities.
- Supervising and supporting the research work of Ph.D. scholars, including research planning, experimentation, and publication activities.
- Assisting in the preparation and documentation of SAR/SSR and other accreditation-related requirements for NAAC/NBA.
- Developing CO-PO-PSO mapping frameworks and maintaining outcome attainment records in compliance with Outcome-Based Education (OBE) practices.
- Managing Learning Management Systems (LMS), including Google Classroom, and preparing course materials, lecture notes, assignments, assessments, and instructional resources.
- Coordinating examination-related activities, including internal assessments, laboratory examinations and end-semester examinations
- Carrying out Research and Publish Research Papers in SCOPUS/SCI/SCIE/WOS Journals; Publishing Patents, Copy Rights etc.
- Contributing to curriculum development, academic administration, quality assurance initiatives, and continuous improvement processes.
- Managing laboratory operations and infrastructure, including maintenance, calibration, manuals, stock and dead-stock registers, asset documentation, safety and quality standards

Teaching Assistant, Electrical Engineering

(July 2023 – June 2024)

Sardar Vallabhbhai National Institute of Technology, Surat

Responsibilities:

- Conducting theory, laboratory and tutorial sessions for UG Students.
- Mentoring B.Tech/M.Tech students in minor and major projects.
- Managing Learning Management Systems (LMS), including Google Classroom, and preparing course materials, lecture notes, assignments, assessments, and instructional resources.

- Coordinating and assisting in examination related activities during internal assessments, practical and end-semester examinations.

Guest Faculty, Electrical Engineering

(August 2017 – May 2018)

Sardar Vallabhbhai National Institute of Technology, Surat

Responsibilities:

- Conducting theory, laboratory and tutorial sessions for UG Students.
- Mentoring B.Tech. students in minor and major projects.
- Preparing course materials, lecture notes, assignments, assessments, and instructional resources.
- Coordinating and assisting in examination related activities during internal assessments, practicals and end-semester examinations.

Graduate Engineer Trainee

(July 2013 – July 2014)

Godrej and Boyce Manufacturing Company Limited, Mumbai.

Responsibilities:

- Surveying, erection and commissioning of 440V/11kV/66 kV overhead distribution lines and 11/66 kV substation under R-APDRP scheme for PSPCL, Punjab at Batala site.
- Preparing and assessing BOQs/BOMs, progress reports and technical drawing as per the IEC standards.
- Monitoring of safety and quality at the site as the per standards.
- Coordinating with clients for site inspection, monitoring and billing.

EDUCATIONAL QUALIFICATIONS

Doctor of Philosophy (Ph.D.) in Electrical Engineering

July 2018 – April 2023

National Institute of Technology, Jamshedpur, India

Cumulative GPA: 7.50/10.00

Ph.D. doctoral research on “Performance Evaluation and Techno Economical Analysis of a Photovoltaic based Distributed Generation System” under the guidance of Dr. Kumari Namrata, Associate Professor, Electrical Engineering, NIT Jamshedpur.

Master of Technology (M.Tech.) in Power Electronics and Drives

August 2015 – June 2017

National Institute of Technology, Jamshedpur, India

Cumulative GPA: 8.70/10.00

Master’s thesis on “A control technique for integrating hybrid renewable energy sources in an islanded microgrid under non-linear and unbalanced load conditions” under the guidance of Dr. Kumari Namrata, Associate Professor, Electrical Engineering, NIT Jamshedpur.

B.Tech. in Electrical and Electronics Engineering

August 2009 – June 2013

S.R.M University, Chennai, India

Cumulative GPA: 8.66/10.00

RESEARCH/ACADEMIC RECOGNITIONS

- Qualified GATE 2015 with an AIR 2841 and GATE Score 551.
- Invited to deliver an Expert Lecture on “Performance Evaluation Techno-Economic Analysis of a Solar based Smart Grid System” for TEQIP-III sponsored workshop on “Advanced Micro Grid, Operation and Control (AMGOC 2020)” held at Veer Surendra Sai University of Technology, Burla, Odisha from 8th-12th September, 2020.
- Invited as a Reviewer to assist in peer review process for articles submitted in the following journals:
 - Optimal Control Applications and Methods, Wiley (SCIE indexed)
 - IEEE Access (SCIE indexed)
 - Arabian Journal for Science and Engineering (AJSE), Springer (SCIE indexed)
 - International Journal of Intelligent Engineering Informatics (SCOPUS indexed)
- Selected for research paper presentation in renowned international conferences such as i-Pact 2017 (VIT Vellore), CAEEC 2019 (IIIT Allahabad), GUCON 2019 (Galgotias University), WIECON-ECE 2020 (IEEE Bangladesh Section), ICPC2T 2022 (NIT Raipur) and ICSEAPT 2021 (NIT Jamshedpur).

SKILLS

- **Programming:** C/C++, MATLAB, Python, Assembly language, Embedded C
- **Frameworks/Libraries:** MATPOWER, Pytorch Simulink, ETAP, DiGSILENT PowerFactory
- **Optimization Tools:** CVX-Convex programming, GUROBI-Linear/Non-linear programming
- **Documentation:** MS Word, Latex/Overleaf, Microsoft Visio, MS Powerpoint, MS Excel

RESEARCH PUBLICATION SUMMARY (List of Publications is also attached)

Research Outcomes	Total	Indexed in SCOPUS	Indexed in SCI/SCIE	Indexed in WOS
Total No. of Journal Publications	12	12	9	11
Total No. of Conference Publications	14	12	--	9
Total No. of Book/Book Chapters	01	01	--	01

Citation Outcomes	As per Google Scholar	As per SCOPUS Database
Total no. of citations	253	213
<i>h-index & i-index</i>	9 & 8	8 & 8

PATENTS

- Design patent on “ROBOT FOR FARM ANALYSIS”. Indian Patent Application No. 416579-001, Registration No. L-156859/2024, Diary No. 16347/2024-CO/L, Date of Application: 11 April 2024 and Date of Issue: 04 July 2024. Journal No: 27/2024 and Journal Date: 05/07/2024 [Published]
- Design patent on “AI BASED DEVICE FOR HIGH-SPEED DATA TRANSMISSION”. Indian Patent Application No.496392-001, Date of Application: 27 March 2026. [Filed]

PROFILE SUMMARY

- **Member, Board of Studies (BOS)**, EE Dept., Sandip University, Nashik (Jan 2025 - Apr 2026)
- **Ph.D. Coordinator, EE Department**, SOET, Sandip University, Nashik (Nov 2024 - Apr 2026)
- **M.Tech. Coordinator**, EE Dept, SOET, Sandip University, Nashik (Nov 2024 - June 2025)
- **NAAC Criteria 3 Coordinator**, EE Dept, Sandip University, Nashik (August 2024 - June 2025)
- **NBA Criteria 3 & 6 Coordinator**, EE Dept, Sandip University, Nashik (August 2025 - Apr 2026)
- **Lab In-charge**, Electrical Machines Laboratory, EE Dept, Sandip University Nashik (Jan 2025 - Apr 2026)

PERSONAL DETAILS

Full Name: Dr. Akshit Samadhiya
Gender: Male
Date of Birth/Age: 14 July 1992, 33 years & 11 Months
Nationality: Indian
Fathers Name: Mr. Rakesh Samadhiya, Retd. Senior Manager, NTPC Limited, India
Permanent Address: L-2/259, Sector-G, LDA Colony, Lucknow, Uttar Pradesh, India-226012

REFERENCES

Dr. Kumari Namrata

Associate Professor, Electrical Engineering
National Institute of Technology, Jamshedpur, India
E-mail: namrata.ee@nitjsr.ac.in

Dr. Sanjeevikumar Padmanaban

Full Professor, Department of Electrical Engineering
University of South-Eastern Norway, Norway
E-mail: sanjeev.padma@usn.no

I declare that the information furnished in the resume is true to the best of my knowledge.

Date: 25-07-2026

Dr. Akshit Samadhiya